

#4

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

**PRELIMINARY AMENDMENT ACCOMPANYING APPLICATION**

APPLICANT: Kunihiro Take. ATTY. DOCKET NO. 09792909-4718

SERIAL NO.

DATE FILED:

INVENTION: "SEMICONDUCTOR WAFER EXAMINATION SYSTEM"

Assistant Commissioner of Patents  
Washington, D.C. 20231

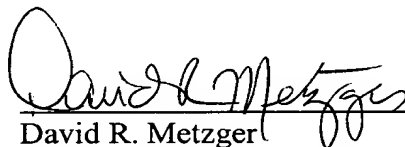
S I R:

Between the title and the heading "Background of the Invention" on page 1, insert the following:

--RELATED APPLICATION DATA

The present application claims priority to Japanese Application No. P11-348080 filed December 7, 1999, which application is incorporated herein by reference to the extent permitted by law.

Respectfully submitted,



(Reg. No. 32,919)

David R. Metzger  
SONNENSCHN NATH & ROSENTHAL  
P.O. Box #061080  
Wacker Drive Station  
Sears Tower  
Chicago, IL 60606-1080  
Customer #26263

Attorneys for Applicant(s)

09792909-4718